Two-channel Power Amplifier for Radio Cassette Players (No Heat Sink Needed during 9 V Operation)

Overview

The LA4598 is a two-channel power IC that is intended for use in portable audio equipment. Needing no heat sink during 9 V operation facilitates set design with a small footprint.

Functions

• Thermal shutdown protector built in.
• Standby switch built in.

Features

• No heat sink needed during 9 V operation
• \( P_O = 2.9 \, W \times 2 \) (\( V_{CC} = 9 \, V \), \( R_L = 3.2 \, \Omega \), THD = 10%).
• Less quiescent current. (\( V_{CC} = 9 \, V \), 20 mA, typ).
• Operating voltage range: \( V_{CC \, op} = 4.2 \) to 16 V.

Specifications

Maximum Ratings at \( T_a = 25 ^\circ C \)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Conditions</th>
<th>Ratings</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Maximum supply voltage</td>
<td>( V_{CC , max} )</td>
<td></td>
<td>18</td>
<td>V</td>
</tr>
<tr>
<td>Allowable power dissipation</td>
<td>( P_d , max^* )</td>
<td>No heat sink</td>
<td>3.6</td>
<td>W</td>
</tr>
<tr>
<td>Junction temperature</td>
<td>( T_j , max )</td>
<td></td>
<td>+150</td>
<td>°C</td>
</tr>
<tr>
<td>Operating temperature</td>
<td>( T_{opr} )</td>
<td></td>
<td>–20 to +75</td>
<td>°C</td>
</tr>
<tr>
<td>Storage temperature</td>
<td>( T_{stg} )</td>
<td></td>
<td>–40 to +150</td>
<td>°C</td>
</tr>
</tbody>
</table>

* With Sanyo-recommended board (9.0 cm × 8.5 cm × 1.5 mm (thickness))
## Operating Conditions at Ta = 25 °C

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Conditions</th>
<th>Ratings</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Recommended supply voltage</td>
<td>$V_{CC}$</td>
<td></td>
<td>9</td>
<td>V</td>
</tr>
<tr>
<td>Recommended load resistance</td>
<td>$R_L$</td>
<td></td>
<td>3.2</td>
<td>Ω</td>
</tr>
<tr>
<td>Operating voltage range</td>
<td>$V_{CC_{op}}$</td>
<td></td>
<td>4.2 to 16.0</td>
<td>V</td>
</tr>
</tbody>
</table>

## Operating Characteristics at Ta = 25 °C, $V_{CC} = 9$ V, $f = 1$ kHz, $R_g = 600$ Ω, $R_L = 3.2$ Ω

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Conditions</th>
<th>min</th>
<th>typ</th>
<th>max</th>
<th>unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Quiescent current</td>
<td>$I_{CCO}$</td>
<td>THD = 10%</td>
<td>10</td>
<td>20</td>
<td>40</td>
<td>mA</td>
</tr>
<tr>
<td>Voltage gain</td>
<td>$V_G$</td>
<td></td>
<td>47</td>
<td>49</td>
<td>51</td>
<td>dB</td>
</tr>
<tr>
<td>Output power</td>
<td>$P_{O1}$</td>
<td>THD = 10%, $R_L = 4$ Ω</td>
<td>2.2</td>
<td>2.9</td>
<td></td>
<td>W</td>
</tr>
<tr>
<td></td>
<td>$P_{O2}$</td>
<td></td>
<td>2.3</td>
<td></td>
<td></td>
<td>W</td>
</tr>
<tr>
<td>Total harmonic distortion</td>
<td>THD</td>
<td>$V_O = 2$ V</td>
<td>0.3</td>
<td>1.0</td>
<td></td>
<td>%</td>
</tr>
<tr>
<td>Input resistance</td>
<td>$r_i$</td>
<td></td>
<td>20</td>
<td>30</td>
<td></td>
<td>kΩ</td>
</tr>
<tr>
<td>Output noise voltage</td>
<td>$V_{NO1}$</td>
<td>$R_g = 0$, B.P.F = 20 Hz to 20 kHz</td>
<td>0.4</td>
<td>1.0</td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td>$V_{NO2}$</td>
<td>$R_g = 10$ kΩ, B.P.F = 20 Hz to 20 kHz</td>
<td>0.6</td>
<td>2.0</td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td>Ripple rejection ratio</td>
<td>$R_r$</td>
<td></td>
<td>40</td>
<td>50</td>
<td></td>
<td>dB</td>
</tr>
<tr>
<td>Channel separation</td>
<td>CH Sep</td>
<td>$R_g = 10$ kΩ, $V_O = 0$ dB</td>
<td>45</td>
<td>55</td>
<td></td>
<td>dB</td>
</tr>
<tr>
<td>Standby current</td>
<td>$I_{sd}$</td>
<td></td>
<td>10</td>
<td></td>
<td></td>
<td>μA</td>
</tr>
</tbody>
</table>

## Block Diagram

![Block Diagram](image)

**Sample Print Pattern**

[Image of sample print pattern]

Copper-foiled side 85 × 90 mm²

Unit (resistance: Ω, capacitance: F)
Sample Application Circuit

To large signal GND

C₁, C₈: Bootstrap capacitors
These capacitors affect low-region output; if the capacitor value is reduced, the low-region output decreases. Therefore, 47 µF or more is desirable.

C₂, C₆: Output capacitors
If the capacitor value is reduced, low-region roll-off frequency \( f_L \) and low-region Po worsen.

C₃, C₇: Oscillation blocking capacitors
Mylar capacitor, which is excellent in temperature characteristics and frequency characteristics is used.

C₄: Power supply capacitor
The capacitor values depends on the power supply line loads (motor, and the like.) and transformer ripple component. 1000 µF to 2200 µF is recommended.

C₅: Standby capacitor
Pop noise reduction capacitor

C₉, C₁₁: Feedback capacitors
In addition to affecting low-region roll-off frequency \( f_L \), if the capacitor value is increased, the start-up time is extended.

C₁₀: Decoupling capacitor
This capacitor absorbs power supply ripples; 220 µF is recommended.

R₁, R₂: Oscillation blocking resistors
The resistor value may be varied 3.3 Ω to 1.0 Ω.

※: Mylar capacitor
C + R can be added to the negative side of the output capacitor.
However, this is true only for a Sanyo-recommended board; for a set board, artwork must also be considered.

Description of External Components

C₁, C₈: Bootstrap capacitors
These capacitors affect low-region output; if the capacitor value is reduced, the low-region output decreases. Therefore, 47 µF or more is desirable.

C₂, C₆: Output capacitors
If the capacitor value is reduced, low-region roll-off frequency \( f_L \) and low-region Po worsen.

C₃, C₇: Oscillation blocking capacitors
Mylar capacitor, which is excellent in temperature characteristics and frequency characteristics is used.

C₄: Power supply capacitor
The capacitor values depends on the power supply line loads (motor, and the like.) and transformer ripple component. 1000 µF to 2200 µF is recommended.

C₅: Standby capacitor
Pop noise reduction capacitor

C₉, C₁₁: Feedback capacitors
In addition to affecting low-region roll-off frequency \( f_L \), if the capacitor value is increased, the start-up time is extended.

C₁₀: Decoupling capacitor
This capacitor absorbs power supply ripples; 220 µF is recommended.

R₁, R₂: Oscillation blocking resistors
The resistor value may be varied 3.3 Ω to 1.0 Ω.
BTL Sample Application Circuit

To large signal GND

Mylar capacitor
C + R can be added to the negative side of the output capacitor.
However, this is true only for a Sanyo-recommended board; for a set board, artwork must also be considered.
Features and Usage Notes

1. VG can be lowered by adding an $R_{NF'}$ to the NF pins (pins 5 and 11).
   Calculated as follows:
   \[ VG = 20 \log \left( \frac{R_f}{R_{NF} + R_{NF'}} \right) \]
   The IC contains $R_f = 20 \, k\Omega$, $R_{NF} = 62 \, \Omega$.
   However, the following must be noted:
   a) If $R_{NF'}$ is added, the ripple bypass effect due to the NF capacitor will worsen, resulting in a worsening of ripple rejection.
   b) Oscillation stability requires, use at 40 dB or less to be avoided.

2. Pin 9 is intended for standby. It is used in conjunction with power supply pin 4. However, it should be noted that when power supply pin 9 and pin 4 are used for separate systems, the output power is affected by the pin 9 supply voltage.

3. It is recommendable to use no input capacitor. However when rubbing noise generated by the volume control is offensive to the ear, an input capacitor must be inserted.

4. Extreme caution must be exercised when the IC is used in the vicinity of the maximum ratings, since even a slight variation in conditions may cause the maximum ratings to be exceeded, thereby leading to breakdown.

5. When making the board, refer to the sample printed circuit pattern. No feedback loop must be formed between input and output. Thick and short wiring is required so that no common resistance exists between the preamplifier GND and power amplifier GND.

6. Addition of components as shown below enables use without introducing an increased distortion at $V_{CC}$ of up to approximately 4.5 V. A capacitor of 470 µF is inserted against pop noise.

![Diagram]

Proper Cares in Mounting a Radiator Fin

1. The tightening torque should be in a range from 4 to 6 kg.cm.

2. The spacing between the screw holes of the radiator fin must match the spacing between the screw holes of the IC. With case outline dimensions L and R referred to, the screws must be tightened with the distance between them as close to each other as possible.

![Diagram]

3. The screws to be used must have a head equivalent to the one of truss machine screw or binder machine screw defined by JIS. Washers must also be used to protect the IC case.

4. No foreign matter such as cutting particles should exist between heat sink and radiator fin. When applying grease on the junction surface, it must be applied uniformly on the whole surface.

5. IC lead pins should be soldered to the printed circuit board after the radiator fin is mounted on the IC.
Specifications of any and all SANYO products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer’s products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer’s products or equipment.

SANYO Electric Co., Ltd. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

In the event that any or all SANYO products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of SANYO Electric Co., Ltd.

Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the “Delivery Specification” for the SANYO product that you intend to use.

Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. SANYO believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

This catalog provides information as of December, 1995. Specifications and information herein are subject to change without notice.