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### Features

- **Low idling current (20mA/2 channels)** enabling prolonged battery life.
- **Less dependence of idling current on \( V_{CC} \).**
- **High power (5.3W typ. x2).**
- **High ripple rejection (60dB at steady state).** Since filters are arranged in 3 stages (including 1 stage inside the IC) to attain satisfactory ripple rejection at transient state, ripple occurring at the time of motor start can be prevented from mixing in.
- **Low pop noise at the time of power supply ON/OFF** and good starting balance between both channels (0.6s.) due to built-in pop noise limiter.
- **Pins provided for compensating high frequency resonance.**
- **Low residual noise (0.4mV).**
- **Wide supply voltage range (6 to 24V)** facilitating design of transformer power supply.
- **Built-in thermal shutdown circuit,**
- **Designed so that inverse insertion or short between adjacent pins causes no destruction.**
- **Channel-to-channel mirror image pin assignment and provision of Pre GND, Power GND pins enabling stable operation and facilitating artwork of printed circuit board.**
- **Minimum number of external parts required (9pcs. min., 12pcs. typ.).**
- **Audio muting capability** (for automatic music selection, electronic tuner).

### Package Dimensions

**Monolithic Linear IC**

**5.3W 2-Channel AF Power Amplifier**

**Ordering number:** ENN1164C

**LA4500**

**Features**

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- Audio muting capability

**Specifications**

**Absolute Maximum Ratings** at \( Ta = 25^\circ C \)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Conditions</th>
<th>Ratings</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Maximum supply voltage</td>
<td>( V_{CC} ) max</td>
<td></td>
<td>24</td>
<td>V</td>
</tr>
<tr>
<td>Maximum output current</td>
<td>( I_{O, peak} )</td>
<td>1 channel</td>
<td>2.5</td>
<td>A</td>
</tr>
<tr>
<td>Allowable power dissipation</td>
<td>( P_d ) max</td>
<td>With infinite heat sink</td>
<td>15</td>
<td>W</td>
</tr>
<tr>
<td>Operating temperature</td>
<td>( T_{op} )</td>
<td></td>
<td>–20 to +75</td>
<td>(^\circ C)</td>
</tr>
<tr>
<td>Storage temperature</td>
<td>( T_{stg} )</td>
<td></td>
<td>–40 to +150</td>
<td>(^\circ C)</td>
</tr>
</tbody>
</table>

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**SANYO Electric Co., Ltd. Semiconductor Company**

TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

21500TN (KT)/N2996RM/9036KII/8064KII No.1164-1/12
### Recommended Operating Conditions at Ta = 25°C

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Conditions</th>
<th>Ratings</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply voltage</td>
<td>(V_{CC})</td>
<td>Stereo</td>
<td>12</td>
<td>V</td>
</tr>
<tr>
<td>Load resistance</td>
<td>(R_L)</td>
<td>Stereo</td>
<td>3</td>
<td>Ω</td>
</tr>
</tbody>
</table>

### Operating Characteristics at Ta = 25°C, \(V_{CC}=12V\), \(R_L=3\Omega\) (stereo), \(f=1\)kHz, \(R_g=600\Omega\), See specified test circuit.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Conditions</th>
<th>Ratings</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Quiescent current</td>
<td>(i_{CCO})</td>
<td>Stereo</td>
<td>10</td>
<td>mA</td>
</tr>
<tr>
<td>Voltage gain</td>
<td>VG</td>
<td></td>
<td>48</td>
<td>dB</td>
</tr>
<tr>
<td>Voltage gain difference</td>
<td>(\Delta VG)</td>
<td>Channel 1, 2</td>
<td>±1</td>
<td>dB</td>
</tr>
<tr>
<td>Output power</td>
<td>(P_O)</td>
<td>THD=10%</td>
<td>4.5</td>
<td>W</td>
</tr>
<tr>
<td>Total harmonic distortion</td>
<td>THD</td>
<td>(V_O=2V)</td>
<td>0.3</td>
<td>%</td>
</tr>
<tr>
<td>Input resistance</td>
<td>(r_I)</td>
<td></td>
<td>30</td>
<td>kΩ</td>
</tr>
<tr>
<td>Output noise voltage</td>
<td>(V_{NO1})</td>
<td>(R_g=0, f=20Hz) to (20kHz), Band-pass filter</td>
<td>0.4</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td>(V_{NO2})</td>
<td>(R_g=10k\Omega, f=20Hz) to (20kHz), Band-pass filter</td>
<td>0.6</td>
<td>mV</td>
</tr>
<tr>
<td>Ripple rejection</td>
<td>(R_p)</td>
<td>(R_g=0, f=100Hz, V_{in}=0dBm)</td>
<td>50</td>
<td>dB</td>
</tr>
<tr>
<td>Channel separation</td>
<td>ch sep</td>
<td>(R_g=10k\Omega, V_{in}=0dBm)</td>
<td>45</td>
<td>dB</td>
</tr>
</tbody>
</table>

### Equivalent Circuit Block Diagram

![Equivalent Circuit Block Diagram](image)
Sample Application Circuit 1
Description of External Parts

C1 (C1) Feedback capacitors
Related to low roll-off frequency f_L for −3dB (100µF, f_L =60Hz).
A capacitance value of 47µF to 100µF is recommended. Increasing the capacitance value makes the starting time (t_s) later. Decreasing the capacitance value makes the starting time (t_s) earlier.

C3 (C4) Bootstrap capacitors
Decreasing the capacitance value lowers output at low frequencies. A capacitance value of 47µF to 100µF is recommended.

C5 (C6) Oscillation blocking capacitors
Polyester film capacitor, being excellent in temperature characteristic, frequency characteristics, is recommended.

C7 (C8) Output capacitors
Related to low roll-off frequency and output at low frequencies. BTL applications normally require output capacitors.

C9 (C10) Switching distortion compensating capacitors
Compensates switching distortion which occurs at a high frequency of 10kHz. Ceramic capacitor of 0.01µF is recommended. If no problem arises in terms of radio-cassette recorder design or tone, it is unnecessary to use these capacitors.

C11 Filter capacitor (A)
Ripple filter circuit provided in power supply line. A capacitance value of 220µF is recommended.
Ripple rejection SVRR starts to be saturated at 47µF. The starting time and pop noise generated at the time of power supply ON must be considered when fixing the capacitance value. A capacitance value of 100µF to 220µF is usable.

C12 Filter capacitor (B)
Ripple filter circuit provided in bias circuit. A capacitance value of 100µF is recommended. 3V suffices the breakdown voltage of this capacitor. This capacitor is for ripple rejection at transient state and rejects noise “buzz” generated when the above-mentioned filter circuit provided in power supply line is saturated due to large ripple and supply voltage drop induced at the time of start of the motor connected to power supply line. If the motor is satisfactory in performance and the power supply regulation including ripple is 500mVrms or less, it is unnecessary to use this capacitor. If noise “buzz” is not offensive to the ear, it is unnecessary to use this capacitor. In this case, other basic performances are not affected adversely.

Features of IC Contents and Functions of Other Pins

(a) Since the input circuit uses PNP transistors and the bias voltage is set nearly equal to 0, no input coupling capacitor is required, thereby enabling direct coupling. However, if slider contact noise of the variable resistor presents any problem, connect a capacitor in series with input.

(b) Various ideas embodied in the idling circuit enable reduced I_CCO and prolonged battery life. Since the non-operating level of the idling circuit is made equal to that of the amplifier, crossover distortion does not worsen at the time of reduced voltage.

(c) The open loop voltage gain is lowered and the negative feedback amount is made small to assure stable operation. Radiation to the radio-frequency stage is made less by soft clipping.

(d) Capacitors for oscillation compensation are contained as a means of reducing the number of external parts. 10pF×2 and 2pF×2 are used. Hig roll-off frequency f_H (−3dB point) depends on these capacitance values.

(e) A thermal shutdown (THD) circuit is contained to prevent the IC from being destroyed by abromal heat generation attributable to insufficient heat dissipation. Pin (11) is used as THD control pin. Biasing pin (11) externally makes the operating temperature lower; and connecting a resister across pin (11) and (10) makes the operating temperature higher. If pin (11) is connected to GND, the thermal shutdown circuit stops operating.

(f) The pin assignment is carefully considered so that no destruction takes place even if power supply is applied at a state where adjacent pins are shorted by solder bridge, etc. Even 180°C-rotated insertion causes no destruction.

(g) Collector pins (5), (16) and base pins (6), (15) for predrive can be conveniently used in applications. For oscillation compensation occuring when operated at a lowered gain, connect a capacitor across the pins (4) and (6) and a capacitor across pins (15) and (16). For fH compensation occuring when operated at a lowered gain, connect a capacitor across pins (4) and (6) and a capacitor across pins (17) and (15). Further soft clipping and prevention of waveform distortion at high frequencies are attained by connecting a series circuit of diode (DS442) and resistor (10kΩ) across pin (6) and GND and the same across pin (15) and GND.

Continued on next page.
Continued from preceding page.

(h) Feedback resistance $R_{NF}$ is contained and the voltage gain is fixed at 50dB so that the variations in the voltage gain can be minimized. The gain can be lowered by connecting $R_{NF}$ externally.

(i) Biasing pin (12) as shown below causes DC audio muting to be applied, thereby cutting off the IC. This makes attack time, recovery time, pop noise, etc. satisfactory.

It is recommended that the following method be used to control the NF pin.

It is recommended that the following method be used to control the NF pin.
LA4500

Channel Separation - f

Ripple Rej - Rg

V_{CC} = 12V
R_h = 3Ω
R_g = 10kΩ
V_{in} = 0dBm
V_{0} = 50dB/1kHz

V_{CC} = 12V
R_h = 3Ω
R_g = 0
V_{in} = 0dBm (775mV)
V_{0} = 0Hz
V_{0} = 50dB/1kHz
DIN AUDIO (20Hz to 20kHz)

Output Ripple Voltage, \text{V}_{rp} - \text{V}_{g}

V_{CC} = 12V
R_h = 3Ω
R_g = 0
V_{in} = 0dBm (775mV)
V_{0} = 0Hz
V_{0} = 50dB/1kHz
DIN AUDIO (20Hz to 20kHz)

Output Ripple Voltage, \text{V}_{rp} - \text{V}_{R}

V_{CC} = 12V
R_h = 3Ω
R_g = 0
V_{in} = 0dBm (775mV)
V_{0} = 0Hz
V_{0} = 50dB/1kHz
DIN AUDIO (20Hz to 20kHz)

Output Ripple Voltage, \text{V}_{rp} - \text{V}_{CC}

V_{CC} = 12V
R_h = 3Ω
R_g = 0
V_{in} = 0dBm (775mV)
V_{0} = 0Hz
V_{0} = 50dB/1kHz
DIN AUDIO (20Hz to 20kHz)

Output Ripple Voltage, \text{V}_{rp} - DC

V_{CC} = 12V
R_h = 3Ω
R_g = 0
V_{in} = 0dBm (775mV)
V_{0} = 0Hz
V_{0} = 50dB/1kHz
DIN AUDIO (20Hz to 20kHz)

Power Supply Ripple Frequency, \text{f}_{r} - Hz

Output Ripple Voltage, \text{V}_{rp} - \text{f}_{r}

V_{CC} = 12V
R_h = 3Ω
R_g = 0
V_{in} = 0dBm (775mV)
V_{0} = 0Hz
V_{0} = 50dB/1kHz
DIN AUDIO (20Hz to 20kHz)

No C12
Sample Application Circuit 2

[Diagram of LA4500 circuit]
Output power (reference value) corresponding to supply voltage and load resistance.

<table>
<thead>
<tr>
<th>System</th>
<th>RL</th>
<th>9V</th>
<th>12V</th>
<th>15V</th>
<th>18V</th>
</tr>
</thead>
<tbody>
<tr>
<td>Stereo</td>
<td>8Ω</td>
<td>1.4W</td>
<td>2.5W</td>
<td>4.0W</td>
<td>5.6W</td>
</tr>
<tr>
<td></td>
<td>6Ω</td>
<td>1.8W</td>
<td>3.2W</td>
<td>5.0W</td>
<td>7.4W</td>
</tr>
<tr>
<td></td>
<td>4Ω</td>
<td>2.4W</td>
<td>4.5W</td>
<td>6.9W</td>
<td>9.8W</td>
</tr>
<tr>
<td></td>
<td>3Ω</td>
<td>3.0W</td>
<td>5.3W</td>
<td>7.8W</td>
<td>–</td>
</tr>
<tr>
<td></td>
<td>2Ω</td>
<td>3.5W</td>
<td>–</td>
<td>–</td>
<td>–</td>
</tr>
<tr>
<td>Bridge</td>
<td>8Ω</td>
<td>4.5W</td>
<td>8.5W</td>
<td>13W</td>
<td>18W</td>
</tr>
<tr>
<td></td>
<td>6Ω</td>
<td>5.5W</td>
<td>9.5W</td>
<td>15W</td>
<td>–</td>
</tr>
<tr>
<td></td>
<td>4Ω</td>
<td>7.0W</td>
<td>–</td>
<td>–</td>
<td>–</td>
</tr>
</tbody>
</table>

(THD=10%)
Proper Cares in Using IC

- **Maximum ratings**
  If the IC is used in the vicinity of the maximum ratings, even a slight variation in conditions may cause the maximum ratings to be exceeded, thereby leading to breakdown. Allow an ample margin of variation for supply voltage, etc. and use the IC in the range where the maximum ratings are not exceeded.

- **Load short**
  If the IC is operated with the short loaded for a long time, breakdown or deterioration may take place. Be sure not to short the load.

- **Printed circuit board**
  When drawing the printed circuit pattern, refer to the sample printed circuit pattern. Be careful not to form a feedback loop between input and output. Make the GND line thick and short so that no common resistance exists between Pre GND and Power GND.

- **When using the IC in radios or radio-cassette tape recorders, allow a good distance between IC and ber antenna. An especially effective measure against radiation to the SW band is to additionally connect a capacitor of 0.033μF (polyester film capacitor) across pins (2) and (20) and across pins (19) and (20) respectively.**

- **Normally connect the heat sink of the package to GND.**

Proper Cares in Mounting Radiator Fin

1. The mounting torque is in the range of 39 to 59N · cm.
2. The distance between screw holes of the radiator fin must coincide with the distance between screw holes of the IC. With case outline dimensions L and R referred to, the screws must be tightened with the distance between them as close to each other as possible.
3. The screw to be used must have a head equivalent to the one of truss machine screw or binder machine screw defined by JIS. Washers must also be used to protect the IC case.
4. No foreign matter such as cutting particles shall exist between heat sink and radiator fin. When applying grease on the junction surface, it must be applied uniformly on the whole surface.
5. IC lead pins are soldered to the printed circuit board after the radiator fin is mounted on the IC.

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