3.5 W 2-Channel AF Power Amplifier for Home Stereos and Music Centers

Features

- Minimum number of external parts required (No input capacitor, bootstrap capacitor required).
- High output: 3.5 W typ. ×2.
- Soft clip, causing little harmonic disturbance to radios (See page 8).
- Small pop noise at the time of power switch ON/OFF (See page 8).
- Built-in protector against abnormal modes (Thermal shutdown, overvoltage).

Package Dimensions

3018A-SIP10F

unit : mm

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Conditions</th>
<th>Ratings</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Maximum supply voltage</td>
<td>VCCmax</td>
<td></td>
<td>25</td>
<td>V</td>
</tr>
<tr>
<td>Maximum output current</td>
<td>IOP</td>
<td>1 channel</td>
<td>2.0</td>
<td>A</td>
</tr>
<tr>
<td>Allowable power dissipation</td>
<td>Pd max</td>
<td>With heat sink (see Pd – Ta characteristics)</td>
<td>7.5</td>
<td>W</td>
</tr>
<tr>
<td>Operating temperature</td>
<td>Topr</td>
<td></td>
<td>–20 to +75</td>
<td>°C</td>
</tr>
<tr>
<td>Storage temperature</td>
<td>Tstg</td>
<td></td>
<td>–40 to +150</td>
<td>°C</td>
</tr>
</tbody>
</table>

Ordering number: EN1321E

Monolithic Linear IC

LA4261

SANYO Electric Co., Ltd. Semiconductor Business Headquarters
TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110 JAPAN

22897HA(A)/71093TS/2126KI/8064KI/8053KI/MT No.1321-1/8
Operating Characteristics at $T_a = 25^\circ C$, $V_{CC} = 16$ V, $R_L = 8 \Omega$, $f = 1$ kHz, $R_g = 600 \Omega$, (circuit 1)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Conditions</th>
<th>min</th>
<th>typ</th>
<th>max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Quiescent current</td>
<td>$I_{CCO}$</td>
<td></td>
<td>46</td>
<td>52</td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>Voltage gain</td>
<td>$V_G$</td>
<td></td>
<td>48</td>
<td>50</td>
<td>52</td>
<td>dB</td>
</tr>
<tr>
<td>Output power</td>
<td>$P_O$</td>
<td>THD = 10%</td>
<td>3.0</td>
<td>3.5</td>
<td></td>
<td>W</td>
</tr>
<tr>
<td>Total harmonic distortion</td>
<td>THD</td>
<td>$P_O = 0.5$ W</td>
<td>0.3</td>
<td>1.0</td>
<td></td>
<td>%</td>
</tr>
<tr>
<td>Output noise voltage</td>
<td>$V_{NO}$</td>
<td>$R_g = 10$ k\Omega, $BW = 20$ Hz to 20 kHz</td>
<td>0.65</td>
<td>1.0</td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td>Ripple rejection ratio</td>
<td>$R_r$</td>
<td>$R_g = 0$, $V_r = 500$ mV</td>
<td>40</td>
<td>50</td>
<td></td>
<td>dB</td>
</tr>
<tr>
<td>Crosstalk</td>
<td>$C_T$</td>
<td>$R_g = 10$ k\Omega</td>
<td>40</td>
<td>55</td>
<td></td>
<td>dB</td>
</tr>
<tr>
<td>Voltage gain difference</td>
<td>$\Delta V_G$</td>
<td></td>
<td>1.5</td>
<td></td>
<td></td>
<td>dB</td>
</tr>
</tbody>
</table>

Equivalent Circuit Block Diagram
**Description of External Parts**

<table>
<thead>
<tr>
<th>Component</th>
<th>Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>C1, C4</td>
<td>100 µF</td>
<td>Feedback capacitor&lt;br&gt;Decreasing the capacitance value lowers the low frequency response. Increasing the capacitance value makes the starting time later.</td>
</tr>
<tr>
<td>C2, C3</td>
<td>330 pF</td>
<td>Input short capacitor&lt;br&gt;Reducer the high frequency noise when the input impedance is increased. Not required when the input impedance is decreased.</td>
</tr>
<tr>
<td>C5, C7</td>
<td>470 µF</td>
<td>Output capacitor&lt;br&gt;Decreasing the capacitance value causes insufficient power at low frequencies.</td>
</tr>
<tr>
<td>C6, C8</td>
<td>0.1 µF polyester film capacitor</td>
<td>Oscillation blocking capacitor&lt;br&gt;Decreasing the capacitance value causes oscillation to occur easily. Use a polyester film capacitor that is good in high frequency response and temperature characteristic. The use of an electrolytic capacitor may cause oscillation to occur at low temperatures.</td>
</tr>
<tr>
<td>C9</td>
<td>470 µF</td>
<td>Power capacitor&lt;br&gt;Decreasing the capacitance value causes ripple to occur. Locating at a distance from the IC or removing this capacitor may cause oscillation to occur.</td>
</tr>
<tr>
<td>C10</td>
<td>100 µF</td>
<td>Ripple filter capacitor&lt;br&gt;Decreasing the capacitance value excessively or removing this capacitor causes ripple to occur. However, increasing the capacitance value does not always cause ripple to be reduced. Decreasing the capacitance value makes the starting time earlier.</td>
</tr>
<tr>
<td>R1, R2</td>
<td>100 kΩ</td>
<td>Input bias resistor&lt;br&gt;Determines the bias (bias of GND potential) to be applied to the input pin and the input impedance. Not required if variable resistors are used.</td>
</tr>
<tr>
<td>R3, R4</td>
<td>3.3 Ω</td>
<td>Resistor connected in series with oscillation blocking capacitor.&lt;br&gt;Prevents phase shift attributable to the oscillation blocking capacitor so that oscillation is hard to occur.</td>
</tr>
</tbody>
</table>
Note for Changing Voltage Gain

Basically, the voltage gain can be reduced by adding external resistors ($R_{NF}$) in series with feedback capacitors C1, C4. However, it should be noted that since there is no phase compensation pin the frequency response is extended and oscillation is liable to occur when the voltage gain is reduced. The voltage gain must not be reduced to be less than 30 dB.

External Muting

If external muting is required, make the circuit as shown right. In this case, the attack time, recovery time, and pop noise are similar to those which occur at the time of power switch ON/OFF.

Proper Cares in Using IC

* Maximum ratings
  If the IC is used in the vicinity of the maximum ratings, even a slight variation in conditions may cause the maximum ratings to be exceeded, thereby leading to breakdown. Allow an ample margin of variation for supply voltage, etc. and use the IC in the range where the maximum ratings are not exceeded.

* Pin-to-pin short
  If power is applied when the space between pins is shorted, breakdown or deterioration may occur. When mounting the IC on the board or applying power, make sure that the space between pins is not shorted with solder, etc.

* When using in radios, allow a sufficient space between IC and bar antenna.

* Printed circuit pattern
  When designing the printed circuit pattern, make the power supply, output, and ground lines thick and short and arrange the pattern and parts so that no feedback loop is formed between input and output. Place power capacitor C9, oscillation blocking capacitors C6, C8 as close to IC pins as possible to prevent oscillation from occurring. Refer to the sample printed circuit pattern.
VNO – Rg

Output noise voltage, VNO – mV

VCC = 16V
Rg = 8Ω
FILTER 20 Hz to 20 kHz
(DIN AUDIO)

Signal source resistance, Rg – Ω

Vro – VCC

Output ripple voltage, Vro – mV

R = 8Ω
Rg = 0
fR = 100Hz

Supply voltage, VCC – V

Vro – Vr

Output ripple voltage, Vro – mV

VCC = 16V
Rg = 0
fR = 100Hz

Ripple voltage, Vr – V

CT – f

Crosstalk, CT – dB

VCC = 16V
Rg = 8Ω
Rg = 10kΩ
V0 = 0dBm

Frequency, f – Hz

ICCO – Ta

Quiescent current, ICCO – mA

VCC = 16V
25V, 22V, 14V, 16V

Ambient temperature, Ta – °C

VG – RNF

Voltage gain, VG – dB

VCC = 16V
Rg = 8Ω
f = 1kHz

External resistance, RNF – Ω

THD – f

Total harmonic distortion, THD – %

VCC = 16V
Rg = 8Ω
V0 = 0.5W

Frequency, f – Hz

No.1321-6/8
Total harmonic distortion, THD – %
Voltage gain, VG – dB

Output noise voltage, V_{NO} – mV
Voltage gain, VG – dB

Output ripple voltage, V_{rp} – mV
Voltage gain, VG – dB
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