HD74HC131

3-to-8-line Decoder/Demultiplexer with Edge-Triggered Address Registers

HITACHI

Description

The HD74HC131 is 3-to-8 linedecoder. It has Address select inputs (A,B,C) and D type register.

Address select data store to D type registers, during the positive going transition of the clock pulse.

Output control (G_1, $\overline{G_2}$) are independent of select input and CLK input, and when G_1 is low or $\overline{G_2}$ = High, all outputs is high.

Features

- High Speed Operation: t_{pd} (CLK to Y) = 20 ns typ (C_L = 50 pF)
- High Output Current: Fanout of 10 LSTTL Loads
- Wide Operating Voltage: V_{CC} = 2 V to 6 V
- Low Input Current: 1 µA max
- Low Quiescent Supply Current: I_{CC} (static) = 4 µA max (Ta = 25°C)

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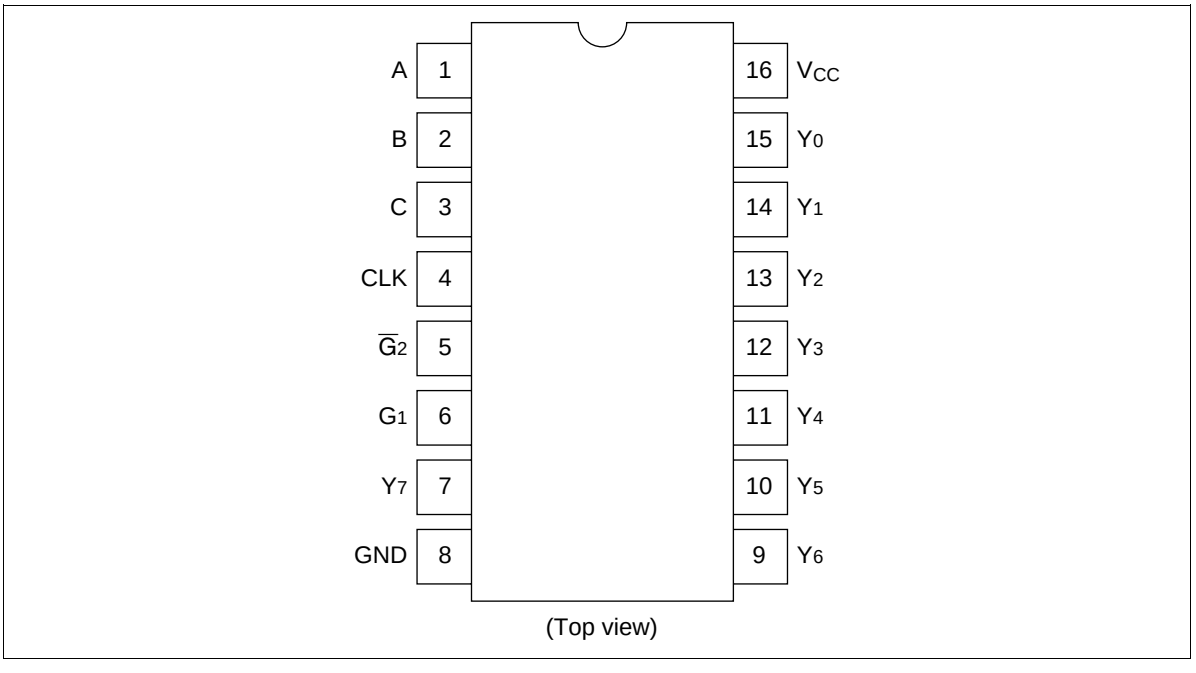
Function Table

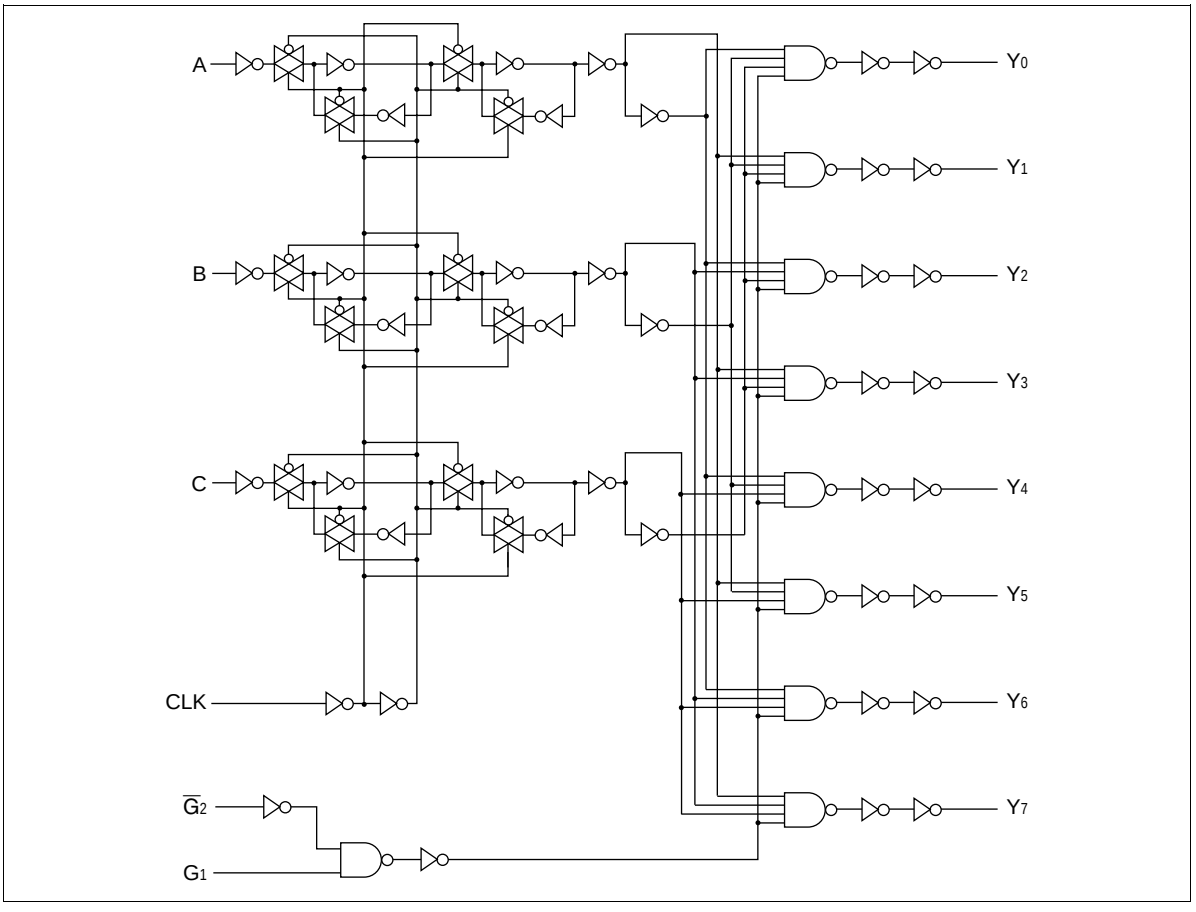
Inputs													
Enable			**Select**			**Outputs**							
CLK	**G1**	**$\overline{G}_2$**	**C**	**B**	**A**	**Y_0**	**Y_1**	**Y_2**	**Y_3**	**Y_4**	**Y_5**	**Y_6**	**Y_7**
X	X	H	X	X	X	H	H	H	H	H	H	H	H
X	L	X	X	X	X	H	H	H	H	H	H	H	H
⎍	H	L	L	L	L	L	H	H	H	H	H	H	H
⎍	H	L	L	L	H	H	L	H	H	H	H	H	H
⎍	H	L	L	H	L	H	H	L	H	H	H	H	H
⎍	H	L	L	H	H	H	H	H	L	H	H	H	H
⎍	H	L	H	L	L	H	H	H	H	L	H	H	H
⎍	H	L	H	L	H	H	H	H	H	H	L	H	H
⎍	H	L	H	H	L	H	H	H	H	H	H	L	H
⎍	H	L	H	H	H	H	H	H	H	H	H	H	L
L	H	L	X	X	X	Outputs corresponding to stored address, L; all others H							

Pin Arrangement



(Top view)

Logic Diagram



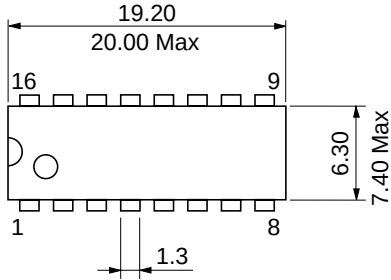
DC Characteristics

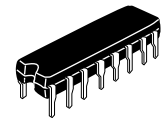
Item	Symbol	V_{CC} (V)	Ta = 25°C Min	Typ	Max	Ta = –40 to +85°C Min	Max	Unit	Test Conditions	
Input voltage	V_{IH}	2.0	1.5	—	—	1.5	—	V		
		4.5	3.15	—	—	3.15	—			
		6.0	4.2	—	—	4.2	—			
	V_{IL}	2.0	—	—	0.5	—	0.5	V		
		4.5	—	—	1.35	—	1.35			
		6.0	—	—	1.8	—	1.8			
Output voltage	V_{OH}	2.0	1.9	2.0	—	1.9	—	V	Vin = V_{IH} or V_{IL}	I_{OH} = –20 µA
		4.5	4.4	4.5	—	4.4	—			
		6.0	5.9	6.0	—	5.9	—			
		4.5	4.18	—	—	4.13	—			I_{OH} = –4 mA
		6.0	5.68	—	—	5.63	—			I_{OH} = –5.2 mA
	V_{OL}	2.0	—	0.0	0.1	—	0.1	V	Vin = V_{IH} or V_{IL}	I_{OL} = 20 µA
		4.5	—	0.0	0.1	—	0.1			
		6.0	—	0.0	0.1	—	0.1			
		4.5	—	—	0.26	—	0.33			I_{OL} = 4 mA
		6.0	—	—	0.26	—	0.33			I_{OL} = 5.2 mA
Input current	Iin	6.0	—	—	±0.1	—	±1.0	µA	Vin = V_{CC} or GND	
Quiescent supply current	I_{CC}	6.0	—	—	4.0	—	40	µA	Vin = V_{CC} or GND, Iout = 0 µA	

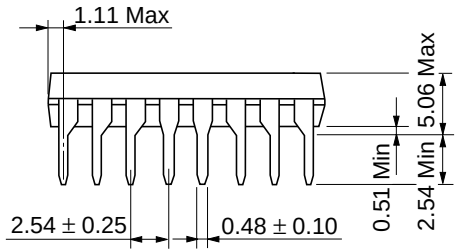
AC Characteristics (C_L = 50 pF, Input $t_r = t_f$ = 6 ns)

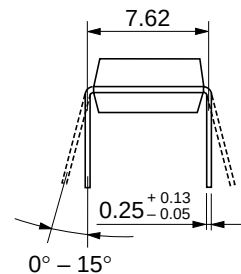
			Ta = 25°C			Ta = –40 to +85°C			
Item	**Symbol**	**V_{cc} (V)**	**Min**	**Typ**	**Max**	**Min**	**Max**	**Unit**	**Test Conditions**
Propagation delay time	t_{PLH}	2.0	—	—	210	—	265	ns	CLK to Y
	t_{PHL}	4.5	—	20	42	—	53		
		6.0	—	—	36	—	45		
	t_{PLH}	2.0	—	—	140	—	175	ns	G_1 or $\overline{G}_2$ to Y
	t_{PHL}	4.5	—	15	28	—	35		
		6.0	—	—	24	—	30		
Pulse width	t_w	2.0	80	—	—	100	—	ns	
		4.5	16	5	—	20	—		
		6.0	14	—	—	17	—		
Setup time	t_{su}	2.0	50	—	—	65	—	ns	
		4.5	10	2	—	13	—		
		6.0	9	—	—	11	—		
Hold time	t_h	2.0	5	—	—	5	—	ns	
		4.5	5	–1	—	5	—		
		6.0	5	—	—	5	—		
Output rise/fall time	t_{TLH}	2.0	—	—	75	—	95	ns	
	t_{THL}	4.5	—	5	15	—	19		
		6.0	—	—	13	—	16		
Input capacitance	Cin	—	—	5	10	—	10	pF	

Unit: mm



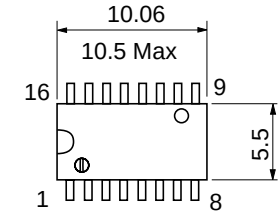


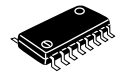


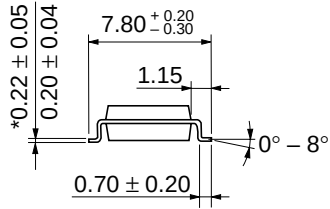


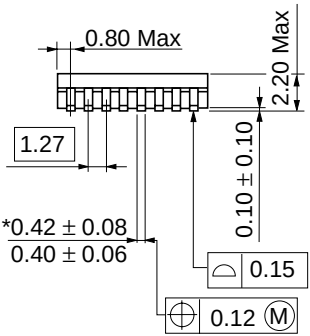
Hitachi Code	DP-16
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	1.07 g

Unit: mm





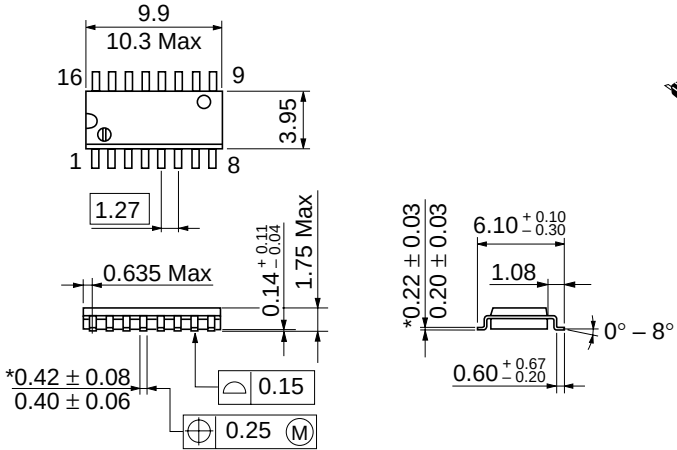




*Dimension including the plating thickness
Base material dimension

Hitachi Code	FP-16DA
JEDEC	—
EIAJ	Conforms
Weight (reference value)	0.24 g

Unit: mm



Hitachi Code	FP-16DN
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.15 g

*Dimension including the plating thickness
Base material dimension